

Antiferromagnetic Tunnel Junctions for future Gb SOT-MRAM

Weisheng Zhao,¹ Kewen Shi,¹ Daoqian Zhu,¹ and Wenlong Cai¹

¹*School of Integrated Circuit Science and Engineering,
Beihang University, Beijing 100191, China*

Over the past decades, magnetic random-access memory (MRAM) based on electron spin has achieved revolutionary progress. In response to the growing demand for ultra-fast, high-capacity non-volatile memory driven by artificial intelligence, spin-orbit torque MRAM (SOT-MRAM), which employs third-generation write technology, has gained significant attention from both academia and industry. Building upon our self-developed 8-inch manufacturing platform, we have successfully fabricated the world's first 8 Mb SOT-MRAM, which achieves significant breakthroughs in storage capacity and writes speed, and effectively guides the technological roadmap for next-generation MRAM development. However, conventional ferromagnetic (FM)-based MRAM faces bottlenecks in achieving high density and scaled feature sizes due to stray field limitations and the low thermal stability. Consequently, antiferromagnetic spintronics has emerged as a new research topic for overcoming existing barriers. Herein, we demonstrate field-free ultrafast electrical writing in antiferromagnetic random-access memory (ARAM) and report the development of the world's first 128 kb spin-orbit torque antiferromagnetic memory (SOT-ARAM). The device uniquely combines the high-density advantage of zero stray field with ultrafast write capability enabled by high intrinsic precession frequency. This breakthrough offers a critical technological pathway toward next-generation Gb-level memory chips.

References:

- [1] Du, A. et al. Electrical manipulation and detection of antiferromagnetism in magnetic tunnel junctions. *Nature Electronics* 6, 425–433 (2023).
- [2] Xiong, D. et al., First CMOS-Integrated 128 Kb Antiferromagnet-Based MRAM with Immunity to 3 T Magnetic Fields. *IEEE International Electron Devices Meeting (IEDM)*, 1-4, (2024).
- [3] Truth Memory Corporation developed the world's first 8Mb SOT-MRAM chip using a 110 nm process. <https://www.mram-info.com/truth-memory-corporation-developed-worlds-first-8mb-sot-mram-chip-using-110-nm-process>.
- [4] Zhu, D. Q. et al. First demonstration of three terminal MRAM devices with immunity to magnetic fields and 10 ns field free switching by electrical manipulation of exchange bias. *IEEE International Electron Devices Meeting (IEDM)* 17.5.1-17.5.4, (2021).